

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

S. MASAKI

Application No.: 10/705,977



Confirmation No.: 6780

Examiner: P. Wamsley

Art Unit: 2819

Filed: November 13, 2003

Attorney Dkt. No.: 100021-00134

For: MULTIPLEXER CIRCUIT FOR CONVERTING PARALLEL DATA INTO SERIAL DATA AT HIGH SPEED AND SYNCHRONIZING THE SERIAL DATA WITH A CLOCK SIGNAL

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 31, 2005

Sir:

In response to the Election/Restriction Requirement mailed January 7, 2005, the Applicant hereby elects, without traverse, Group I, Figure 4, directed to claims 1-4 and 11-15.

In view of the Applicant's above election, Applicant respectfully submits that the Restriction Requirement has been satisfied. Accordingly, Applicant respectfully requests examination of elected claims 1-4 and 11-15 on the merits.

Applicant respectfully submits that claims 1-3, 11 and 14-15 are generic to all species. Accordingly, Applicant, upon the allowance of at least one of generic claims 1-3, 11 and 14-15, retains the right to request rejoinder of the claims directed to the non-elected species as provided by 37 C.F.R. § 1.141. Furthermore, Applicant reserves the right to file one or more divisional applications directed to the non-elected claims.

Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300, referring to client-matter number 100021-00134.

Respectfully submitted,



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